

All-Path Reachability Logic [★]

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Abstract. This paper presents a language-independent proof system for reachability properties of programs written in non-deterministic (e.g. concurrent) languages, referred to as *all-path reachability logic*. It derives partial-correctness properties with all-path semantics (a state satisfying a given precondition reaches states satisfying a given postcondition on all terminating execution paths). The proof system takes as axioms any unconditional operational semantics, and is sound (partially correct) and (relatively) complete, independent of the object language; the soundness has also been mechanized (Coq). This approach is implemented in a tool for semantics-based verification as part of the \mathbb{K} framework.

1 Introduction

Operational semantics are easy to define and understand. Giving a language an operational semantics can be regarded as “implementing” a formal interpreter. Operational semantics require little formal training, scale up well and, being executable, can be tested. Thus, operational semantics are typically used as trusted reference models for the defined languages. Despite these advantages, operational semantics are rarely used directly for program verification (i.e. verifying properties of a given program, rather than performing meta-reasoning about a given language), because such proofs tend to be low-level and tedious, as they involve formalizing and working directly with the corresponding transition system. Hoare or dynamic logics allow higher level reasoning at the cost of (re)defining the language as a set of abstract proof rules, which are harder to understand and trust. The state-of-the-art in mechanical program verification is to develop and prove such language-specific proof systems sound w.r.t to a trusted operational semantics [1–3], but that needs to be done for each language separately.

Defining more semantics for the same language and proving the soundness of one semantics in terms of another are highly uneconomical tasks when real programming languages are concerned, often taking several years to complete. Ideally, we would like to have only one semantics for a language, together with a generic theory and a set of generic tools and techniques allowing us to get all the benefits of any other semantics without paying the price of defining other semantics. Recent work [4–7] shows this *is possible*, by proposing a *language-independent proof system* which derives program properties directly from an operational semantics, *at the same proof granularity and compositionality* as a language-specific axiomatic semantics. Specifically, it introduces (*one-path*) *reachability rules*, which generalize both operational semantics reduction

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rules and Hoare triples, and give a proof system which derives new reachability rules (program properties) from a set of given reachability rules (the language semantics).

However, the existing proof system has a major limitation: it only derives reachability rules with a *one-path* semantics, that is, it guarantees a program property holds on one but not necessarily all execution paths, which suffices for deterministic languages but not for non-deterministic (concurrent) languages. We here remove this limitation, proposing the first generic all-path reachability proof system for program verification.

Using *matching logic* [8] as a configuration specification formalism (Section 2), where a pattern φ specifies all program configurations that match it, we first introduce the novel notion of an *all-path reachability rule* $\varphi \Rightarrow^{\forall} \varphi'$ (Section 3), where φ and φ' are matching logic patterns. Rule $\varphi \Rightarrow^{\forall} \varphi'$ is valid iff any program configuration satisfying φ reaches, on any complete execution path, some configuration satisfying φ' . This subsumes partial-correctness in non-deterministic languages. We then present a proof system for deriving an all-path reachability rule $\varphi \Rightarrow^{\forall} \varphi'$ from a set \mathcal{S} of semantics rules (Section 4). \mathcal{S} consists of reduction rules $\varphi_l \Rightarrow^{\exists} \varphi_r$, where φ_l and φ_r are simple patterns as encountered in operational semantics (Section 6), which can be non-deterministic. The proof system derives more general sequents “ $\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^{\forall} \varphi'$ ”, with \mathcal{A} and \mathcal{C} two sets of reachability rules. Intuitively, \mathcal{A} 's rules (*axioms*) are already established valid, and thus can be immediately used. Those in \mathcal{C} (*circularities*) are only claimed valid, and can be used only after taking execution steps based on the rules in \mathcal{S} or \mathcal{A} . The most important proof rules are

Step :

$$\frac{\begin{array}{l} \models \varphi \rightarrow \bigvee_{\varphi_l \Rightarrow^{\exists} \varphi_r \in \mathcal{S}} \exists \text{FreeVars}(\varphi_l) \varphi_l \\ \models \exists c (\varphi[c/\square] \wedge \varphi_l[c/\square]) \wedge \varphi_r \rightarrow \varphi' \quad \text{for each } \varphi_l \Rightarrow^{\exists} \varphi_r \in \mathcal{S} \end{array}}{\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^{\forall} \varphi'}$$

Circularity :

$$\frac{\mathcal{S}, \mathcal{A} \vdash_{C \cup \{\varphi \Rightarrow^{\forall} \varphi'\}} \varphi \Rightarrow^{\forall} \varphi'}{\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^{\forall} \varphi'}$$

STEP is the key proof rule which deals with non-determinism: it derives a sequent where φ reaches φ' in one step on all paths. The first premise ensures that any configuration satisfying φ has successors, the second that all successors satisfy φ' (\square is the configuration placeholder). CIRCULARITY adds the current goal to \mathcal{C} at any point in a proof, and generalizes language-independently the various language-specific axiomatic semantics invariant rules (this form was introduced in [4]).

We illustrate on examples how our proof system enables state exploration (similar to symbolic model-checking), and verification of program properties (Section 6). We show that our proof system is sound and relatively complete (Section 5). We describe our implementation of the proof system as part of the \mathbb{K} framework [9] (Section 7).

Contributions This paper makes the following specific contributions:

1. A language-independent proof system for deriving all-path reachability properties, with proofs of its soundness and relative completeness; the soundness result has also been mechanized in Coq, to serve as a foundation for certifiable verification.
2. An implementation of it as part of the \mathbb{K} framework.

2 Matching Logic

Here we briefly recall matching logic [8], which is a logic designed for specifying and reasoning about arbitrary program and system configurations. A matching logic formula, called a *pattern*, is a first-order logic (FOL) formula with special predicates, called basic patterns. A *basic pattern* is a configuration term with variables. Intuitively, a pattern specifies both structural and logical constraints: a configuration satisfies the pattern iff it matches the structure (basic patterns) and satisfies the constraints.

Matching logic is parametric in a signature and a model of configurations, making it a prime candidate for expressing state properties in a language-independent verification framework. The configuration signature can be as simple as that of IMP (Fig. 3), or as complex as that of the C language [10] (with more than 70 semantic components).

We use basic concepts from multi-sorted first-order logic. Given a *signature* Σ which specifies the sorts and arities of the function symbols (constructors or operators) used in configurations, let $T_\Sigma(\text{Var})$ denote the *free* Σ -algebra of terms with variables in Var . $T_{\Sigma,s}(\text{Var})$ is the set of Σ -terms of sort s . A valuation $\rho : \text{Var} \rightarrow \mathcal{T}$ with \mathcal{T} a Σ -algebra extends uniquely to a (homonymous) Σ -algebra morphism $\rho : T_\Sigma(\text{Var}) \rightarrow \mathcal{T}$. Many mathematical structures needed for language semantics have been defined as Σ -algebras, including: boolean algebras, natural/integer/rational numbers, lists, sets, bags (or multisets), maps (e.g., for states, heaps), trees, queues, stacks, etc.

Let us fix the following: (1) an algebraic signature Σ , associated to some desired configuration syntax, with a distinguished sort Cfg , (2) a sort-wise infinite set Var of variables, and (3) a Σ -algebra \mathcal{T} , the *configuration model*, which may but need not be a term algebra. As usual, \mathcal{T}_{Cfg} denotes the elements of \mathcal{T} of sort Cfg , called *configurations*.

Definition 1. [8] A *matching logic formula*, or a **pattern**, is a first-order logic (FOL) formula which additionally allows terms in $T_{\Sigma,\text{Cfg}}(\text{Var})$, called **basic patterns**, as predicates. A pattern is **structureless** if it contains no basic patterns.

We define satisfaction $(\gamma, \rho) \models \varphi$ over configurations $\gamma \in \mathcal{T}_{\text{Cfg}}$, valuations $\rho : \text{Var} \rightarrow \mathcal{T}$ and patterns φ as follows (among the FOL constructs, we only show \exists):

$$(\gamma, \rho) \models \exists X \varphi \text{ iff } (\gamma, \rho') \models \varphi \text{ for some } \rho' : \text{Var} \rightarrow \mathcal{T} \text{ with } \rho'(y) = \rho(y) \text{ for all } y \in \text{Var} \setminus X$$

$$(\gamma, \rho) \models \pi \quad \text{iff } \gamma = \rho(\pi) \quad \text{where } \pi \in T_{\Sigma,\text{Cfg}}(\text{Var})$$

We write $\models \varphi$ when $(\gamma, \rho) \models \varphi$ for all $\gamma \in \mathcal{T}_{\text{Cfg}}$ and all $\rho : \text{Var} \rightarrow \mathcal{T}$.

A basic pattern π is satisfied by all the configurations γ that *match* it; in $(\gamma, \rho) \models \pi$ the ρ can be thought of as the “witness” of the matching, and can be further constrained in a pattern. For instance, the pattern from Section 6

$$\langle \mathbf{x} := \mathbf{x} + 1 \parallel \mathbf{x} := \mathbf{x} + 1, \mathbf{x} \mapsto n \rangle \wedge (n = 0 \vee n = 1)$$

is matched by the configurations with code “ $\mathbf{x} := \mathbf{x} + 1 \parallel \mathbf{x} := \mathbf{x} + 1$ ” and state mapping program variable \mathbf{x} into integer n with n being either 0 or 1. We use *italic* for mathematical variables in Var and `typewriter` for program variables (program variables are represented in matching logic as constants of sort $P\text{Var}$, see Section 6).

Next, we recall how matching logic formulae can be translated into FOL formulae, so that its satisfaction becomes FOL satisfaction in the model of configurations, \mathcal{T} . Then, we can use conventional theorem provers or proof assistants for pattern reasoning.

Definition 2. [8] Let \square be a fresh Cfg variable. For a pattern φ , let φ^\square be the FOL formula formed from φ by replacing basic patterns $\pi \in T_{\Sigma,\text{Cfg}}(\text{Var})$ with equalities $\square = \pi$. If $\rho : \text{Var} \rightarrow \mathcal{T}$ and $\gamma \in \mathcal{T}_{\text{Cfg}}$ then let the valuation $\rho^\gamma : \text{Var} \cup \{\square\} \rightarrow \mathcal{T}$ be such that $\rho^\gamma(x) = \rho(x)$ for $x \in \text{Var}$ and $\rho^\gamma(\square) = \gamma$.

With the notation in Definition 2, $(\gamma, \rho) \models \varphi$ iff $\rho^\gamma \models \varphi^\square$, and $\models \varphi$ iff $\mathcal{T} \models \varphi^\square$. Thus, matching logic is a methodological fragment of the FOL theory of \mathcal{T} . We drop \square from φ^\square when it is clear in context that we mean the FOL formula instead of the matching logic pattern. It is often technically convenient to eliminate \square from φ , by replacing \square with a *Cfg* variable c and using $\varphi[c/\square]$ instead of φ . We use the FOL representation in the STEP proof rule in Fig. 1, and to establish relative completeness in Section 5.

3 Specifying Reachability

In this section we define one-path and all-path reachability. We begin by recalling some matching logic reachability [6] notions that we need for specifying reachability.

Definition 3. [6] A (one-path) **reachability rule** is a pair $\varphi \Rightarrow^{\exists} \varphi'$, where φ and φ' are patterns (which can have free variables). Rule $\varphi \Rightarrow^{\exists} \varphi'$ is **weakly well-defined** iff for any $\gamma \in \mathcal{T}_{\text{Cfg}}$ and $\rho : \text{Var} \rightarrow \mathcal{T}$ with $(\gamma, \rho) \models \varphi$, there exists $\gamma' \in \mathcal{T}_{\text{Cfg}}$ with $(\gamma', \rho) \models \varphi'$. A **reachability system** is a set of reachability rules. Reachability system \mathcal{S} is **weakly well-defined** iff each rule is weakly well-defined. \mathcal{S} induces a **transition system** $(\mathcal{T}, \Rightarrow_{\mathcal{S}}^{\mathcal{T}})$ on the configuration model: $\gamma \Rightarrow_{\mathcal{S}}^{\mathcal{T}} \gamma'$ for $\gamma, \gamma' \in \mathcal{T}_{\text{Cfg}}$ iff there is some rule $\varphi \Rightarrow^{\exists} \varphi'$ in \mathcal{S} and some valuation $\rho : \text{Var} \rightarrow \mathcal{T}$ with $(\gamma, \rho) \models \varphi$ and $(\gamma', \rho) \models \varphi'$. A $\Rightarrow_{\mathcal{S}}^{\mathcal{T}}$ -**path** is a finite sequence $\gamma_0 \Rightarrow_{\mathcal{S}}^{\mathcal{T}} \gamma_1 \Rightarrow_{\mathcal{S}}^{\mathcal{T}} \dots \Rightarrow_{\mathcal{S}}^{\mathcal{T}} \gamma_n$ with $\gamma_0, \dots, \gamma_n \in \mathcal{T}_{\text{Cfg}}$. A $\Rightarrow_{\mathcal{S}}^{\mathcal{T}}$ -**path** is **complete** iff it is not a strict prefix of any other $\Rightarrow_{\mathcal{S}}^{\mathcal{T}}$ -path.

We assume an operational semantics is a set of (unconditional) reduction rules “ $l \Rightarrow^{\exists} r$ if b ”, where $l, r \in T_{\Sigma, \text{Cfg}}(\text{Var})$ are program configurations with variables and $b \in T_{\Sigma, \text{Bool}}(\text{Var})$ is a condition constraining the variables of l, r . Styles of operational semantics using only such (unconditional) rules include evaluation contexts [11], the chemical abstract machine [12] and \mathbb{K} [9] (see Section 6 for an evaluation contexts semantics). Several large languages have been given semantics in such styles, including C [10] (about 1200 rules) and R5RS Scheme [13]. The reachability proof system works with any set of rules of this form, being agnostic to the particular style of semantics.

Such a rule “ $l \Rightarrow^{\exists} r$ if b ” states that a ground configuration γ which is an instance of l and satisfies the condition b reduces to an instance γ' of r . Matching logic can express terms with constraints: $l \wedge b$ is satisfied by exactly the γ above. Thus, we can regard such a semantics as a particular weakly well-defined reachability system \mathcal{S} with rules of the form “ $l \wedge b \Rightarrow^{\exists} r$ ”. The weakly well-defined condition on \mathcal{S} guarantees that if γ matches the left-hand-side of a rule in \mathcal{S} , then the respective rule induces an outgoing transition from γ . The transition system induced by \mathcal{S} describes precisely the behavior of any program in any given state. In [4–6] we show that reachability rules capture one-path reachability properties and Hoare triples for deterministic languages.

Formally, let us fix an operational semantics given as a reachability system \mathcal{S} . Then, we can specify reachability in the transition system induced by \mathcal{S}

Definition 4. An **all-path reachability rule** is a pair $\varphi \Rightarrow^{\forall} \varphi'$ of patterns φ and φ' .

An all-path reachability rule $\varphi \Rightarrow^{\forall} \varphi'$ is satisfied, $\mathcal{S} \models \varphi \Rightarrow^{\forall} \varphi'$, iff for all complete $\Rightarrow_{\mathcal{S}}^{\mathcal{T}}$ -paths τ starting with $\gamma \in \mathcal{T}_{\text{Cfg}}$ and for all $\rho : \text{Var} \rightarrow \mathcal{T}$ such that $(\gamma, \rho) \models \varphi$, there exists some $\gamma' \in \tau$ such that $(\gamma', \rho) \models \varphi'$.

A one-path reachability rule $\varphi \Rightarrow^{\exists} \varphi'$ is satisfied, $\mathcal{S} \models \varphi \Rightarrow^{\exists} \varphi'$, iff for all $\gamma \in \mathcal{T}_{\text{Cfg}}$ and $\rho : \text{Var} \rightarrow \mathcal{T}$ such that $(\gamma, \rho) \models \varphi$, there is either a $\Rightarrow_{\mathcal{S}}^{\mathcal{T}}$ -path from γ to some γ' such that $(\gamma', \rho) \models \varphi'$, or there is a diverging execution $\gamma \Rightarrow_{\mathcal{S}}^{\mathcal{T}} \gamma_1 \Rightarrow_{\mathcal{S}}^{\mathcal{T}} \gamma_2 \Rightarrow_{\mathcal{S}}^{\mathcal{T}} \dots$ from γ .

<p>Step :</p> $\frac{\begin{array}{l} \models \varphi \rightarrow \bigvee_{\varphi_l \Rightarrow^3 \varphi_r \in \mathcal{S}} \exists \text{FreeVars}(\varphi_l) \varphi_l \\ \models \exists c (\varphi[c/\square] \wedge \varphi_l[c/\square]) \wedge \varphi_r \rightarrow \varphi' \quad \text{for each } \varphi_l \Rightarrow^3 \varphi_r \in \mathcal{S} \end{array}}{\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^V \varphi'}$	<p>Axiom :</p> $\frac{\varphi \Rightarrow^V \varphi' \in \mathcal{A}}{\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^V \varphi'}$
<p>Reflexivity :</p> $\frac{}{\mathcal{S}, \mathcal{A} \vdash \varphi \Rightarrow^V \varphi}$	<p>Transitivity :</p> $\frac{\mathcal{S}, \mathcal{A} \vdash_C \varphi_1 \Rightarrow^V \varphi_2 \quad \mathcal{S}, \mathcal{A} \cup C \vdash \varphi_2 \Rightarrow^V \varphi_3}{\mathcal{S}, \mathcal{A} \vdash_C \varphi_1 \Rightarrow^V \varphi_3}$
<p>Case Analysis :</p> $\frac{\mathcal{S}, \mathcal{A} \vdash_C \varphi_1 \Rightarrow^V \varphi \quad \mathcal{S}, \mathcal{A} \vdash_C \varphi_2 \Rightarrow^V \varphi}{\mathcal{S}, \mathcal{A} \vdash_C \varphi_1 \vee \varphi_2 \Rightarrow^V \varphi}$	<p>Abstraction :</p> $\frac{\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^V \varphi' \quad X \cap \text{FreeVars}(\varphi') = \emptyset}{\mathcal{S}, \mathcal{A} \vdash_C \exists X \varphi \Rightarrow^V \varphi'}$
<p>Consequence :</p> $\frac{\models \varphi_1 \rightarrow \varphi'_1 \quad \mathcal{S}, \mathcal{A} \vdash_C \varphi'_1 \Rightarrow^V \varphi'_2 \quad \models \varphi'_2 \rightarrow \varphi_2}{\mathcal{S}, \mathcal{A} \vdash_C \varphi_1 \Rightarrow^V \varphi_2}$	<p>Circularity :</p> $\frac{\mathcal{S}, \mathcal{A} \vdash_{C \cup \{\varphi \Rightarrow^V \varphi'\}} \varphi \Rightarrow^V \varphi'}{\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^V \varphi'}$

Fig. 1. Proof system for reachability. We make the standard assumption that the free variables of $\varphi_l \Rightarrow^3 \varphi_r$ in the STEP proof rule are fresh (e.g., disjoint from those of $\varphi \Rightarrow^V \varphi'$).

The racing increment example in Section 6 can be specified by

$$\langle x := x + 1 \parallel x := x + 1, x \mapsto m \rangle \Rightarrow^V \exists n (\langle \text{skip}, x \mapsto n \rangle \wedge (n = m +_{Int} 1 \vee n = m +_{Int} 2))$$

which states that every terminating execution reaches a state where execution of both threads is complete and the value of x has increased by 1 or 2 (this code has a race).

A Hoare triple describes the resulting state after execution finishes, so it corresponds to a reachability rule where the right side contains no remaining code. However, all-path reachability rules are strictly more expressive than Hoare triples, as they can also specify intermediate configurations (the code in the right-hand-side need not be empty) Reachability rules provide a unified representation for both language semantics and program specifications: $\varphi \Rightarrow^3 \varphi'$ for semantics and $\varphi \Rightarrow^V \varphi'$ for all-path reachability specifications. Note that, like Hoare triples, reachability rules can only specify properties of complete paths (that is, terminating execution paths). One can use existing Hoare logic techniques to break reasoning about a non-terminating program into reasoning about its terminating components.

4 Reachability Proof System

Fig. 1 shows our novel proof system for all-path reachability. The target language is given as a weakly well-defined reachability system \mathcal{S} . The soundness result (Thm. 1) guarantees that $\mathcal{S} \models \varphi \Rightarrow^V \varphi'$ if $\mathcal{S} \vdash \varphi \Rightarrow^V \varphi'$ is derivable. Note that the proof system derives more general sequents of the form $\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^V \varphi'$, where \mathcal{A} and \mathcal{C} are sets of reachability rules. The rules in \mathcal{A} are called *axioms* and rules in \mathcal{C} are called *circularities*. If either \mathcal{A} or \mathcal{C} does not appear in a sequent, it means the respective set is empty: $\mathcal{S} \vdash_C \varphi \Rightarrow^V \varphi'$ is a shorthand for $\mathcal{S}, \emptyset \vdash_C \varphi \Rightarrow^V \varphi'$, and $\mathcal{S}, \mathcal{A} \vdash \varphi \Rightarrow^V \varphi'$ is a shorthand

for $\mathcal{S}, \mathcal{A} \vdash_{\emptyset} \varphi \Rightarrow^{\forall} \varphi'$. Initially, both \mathcal{A} and C are empty. Note that “ \rightarrow ” in STEP and CONSEQUENCE denotes implication.

The intuition is that the reachability rules in \mathcal{A} can be assumed valid, while those in C have been postulated but not yet justified. After making progress from φ (at least one derivation by STEP or by AXIOM with the rules in \mathcal{A}), the rules in C become (coinductively) valid (can be used in derivations by AXIOM). During the proof, circularities can be added to C via CIRCULARITY, flushed into \mathcal{A} by TRANSITIVITY, and used via AXIOM. The desired semantics for sequent $\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^{\forall} \varphi'$ (read “ \mathcal{S} with axioms \mathcal{A} and circularities C proves $\varphi \Rightarrow^{\forall} \varphi'$ ”) is: $\varphi \Rightarrow^{\forall} \varphi'$ holds if the rules in \mathcal{A} hold and those in C hold after taking at least one step from φ in the transition system $(\Rightarrow_{\mathcal{S}}^{\mathcal{T}}, \mathcal{T})$, and if $C \neq \emptyset$ then φ reaches φ' after at least one step on all complete paths. As a consequence of this definition, any rule $\varphi \Rightarrow^{\forall} \varphi'$ derived by CIRCULARITY has the property that φ reaches φ' after at least one step, due to CIRCULARITY having a prerequisite $\mathcal{S}, \mathcal{A} \vdash_{C \cup \{\varphi \Rightarrow^{\forall} \varphi'\}} \varphi \Rightarrow^{\forall} \varphi'$ (with a non-empty set of circularities). We next discuss the proof rules.

STEP derives a sequent where φ reaches φ' in one step on all paths. The first premise ensures any configuration matching φ matches the left-hand-side φ_l of some rule in \mathcal{S} and thus, as \mathcal{S} is weakly well-defined, can take a step. Formally, if $(\gamma, \rho) \models \varphi$, then there exists some rule $\varphi_l \Rightarrow^{\exists} \varphi_r \in \mathcal{S}$ and some valuation ρ' of the free variables of φ_l such that $(\gamma, \rho') \models \varphi_l$, and thus γ has at least one $\Rightarrow_{\mathcal{S}}^{\mathcal{T}}$ -successor generated by the rule $\varphi_l \Rightarrow^{\exists} \varphi_r$. The second premise ensures that each $\Rightarrow_{\mathcal{S}}^{\mathcal{T}}$ -successor of a configuration matching φ matches φ' . Formally, if $\gamma \Rightarrow_{\mathcal{S}}^{\mathcal{T}} \gamma'$ and γ matches φ then there is some rule $\varphi_l \Rightarrow^{\exists} \varphi_r \in \mathcal{S}$ and $\rho : \text{Var} \rightarrow \mathcal{T}$ such that $(\gamma, \rho) \models \varphi \wedge \varphi_l$ and $(\gamma', \rho) \models \varphi_r$; then the second part implies γ' matches φ' .

Designing a proof rule for deriving an execution step along all paths is non-trivial. For instance, one might expect STEP to require as many premises as there are transitions going out of φ , as is the case for the examples presented later in this paper. However, that is not possible, as the number of successors of a configuration matching φ may be unbounded even if each matching configuration has a finite branching factor in the transition system. STEP avoids this issue by requiring only one premise for each rule by which some configuration φ can take a step, even if that rule can be used to derive multiple transitions. To illustrate this situation, consider a language defined by $\mathcal{S} \equiv \{\langle n_1 \rangle \wedge n_1 >_{Int} n_2 \Rightarrow^{\exists} \langle n_2 \rangle\}$, with n_1 and n_2 non-negative integer variables. A configuration in this language is a singleton with a non-negative integer. Intuitively, a positive integer transits into a strictly smaller non-negative integer, in a non-deterministic way. The branching factor of a non-negative integer is its value. Then $\mathcal{S} \models \langle m \rangle \Rightarrow^{\forall} \langle 0 \rangle$. Deriving it reduces (by CIRCULARITY and other proof rules) to deriving $\langle m_1 \rangle \wedge m_1 >_{Int} 0 \Rightarrow^{\forall} \exists m_2 (\langle m_2 \rangle \wedge m_1 >_{Int} m_2)$. The left-hand-side is matched by any positive integer, and thus its branching factor is infinity. Deriving this rule with STEP requires only two premises, $\models (\langle m_1 \rangle \wedge m_1 >_{Int} 0) \rightarrow \exists n_1 n_2 (\langle n_1 \rangle \wedge n_1 >_{Int} n_2)$ and $\models \exists c (c = \langle m_1 \rangle \wedge m_1 >_{Int} 0 \wedge c = \langle n_1 \rangle \wedge n_1 >_{Int} n_2) \wedge \langle n_2 \rangle \rightarrow \exists m_2 (\langle m_2 \rangle \wedge m_1 >_{Int} m_2)$. A similar situation arises in real life for languages with thread pools of arbitrary size.

AXIOM applies a trusted rule. REFLEXIVITY and TRANSITIVITY capture the corresponding closure properties of the reachability relation. REFLEXIVITY requires C to be empty to ensure that all-path rules derived with non-empty C take at least one step. TRANSITIVITY enables the circularities as axioms for the second premise, since if C is not empty, the

first premise is guaranteed to take at least one step. CONSEQUENCE, CASE ANALYSIS and ABSTRACTION are adapted from Hoare logic. Ignoring circularities, these seven rules discussed so far constitute formal infrastructure for symbolic execution.

CIRCULARITY has a coinductive nature, allowing us to make new circularity claims. We typically make such claims for code with repetitive behaviors, such as loops, recursive functions, jumps, etc. If there is a derivation of the claim using itself as a circularity, then the claim holds. This would obviously be unsound if the new assumption was available immediately, but requiring progress (taking at least one step in the transition system $(\mathcal{T}, \Rightarrow_{\mathcal{G}}^{\tau})$) before circularities can be used ensures that only diverging executions can correspond to endless invocation of a circularity.

One important aspect of concurrent program verification, which we do not address in this paper, is proof compositionality. Our focus here is limited to establishing a sound and complete language-independent proof system for all-path reachability rules, to serve as a foundation for further results and applications, and to discuss our current implementation of it. We only mention that we have already studied proof compositionality for earlier one-path variants of reachability logic [5], showing that there is a mechanical way to translate any Hoare logic proof derivation into a reachability proof of similar size and structure, but based entirely on the operational semantics of the language. The overall conclusion of our previous study, which we believe will carry over to all-path reachability, was that compositional reasoning can be achieved methodologically using our proof system, by proving and then using appropriate reachability rules as lemmas. However, note that this works only for theoretically well-behaved languages which enjoy a compositional semantics. For example, a language whose semantics assumes a bounded heap size, or which has constructs whose semantics involve the entire program, e.g., call/cc, will lack compositionality.

5 Soundness and Relative Completeness

Here we discuss the soundness and relative completeness of our proof system. Unlike the similar results for Hoare logics and dynamic logics, which are separately proved for each language taking into account the particularities of that language, we prove soundness and relative completeness once and for all languages.

Soundness states that a syntactically derivable sequent holds semantically. Because of the utmost importance of the result below, we have also mechanized its proof. Our complete Coq formalization can be found at <http://fsl.cs.illinois.edu/r1>.

Theorem 1 (Soundness). *If $S \vdash \varphi \Rightarrow^{\forall} \varphi'$ then $S \models \varphi \Rightarrow^{\forall} \varphi'$.*

Proof (sketch — complete details in [14]). Unfortunately, due to CIRCULARITY, a simple induction on the proof tree does not work. Instead, we prove a more general result (Lemma 1 below) allowing sequents with nonempty \mathcal{A} and \mathcal{C} , which requires stating semantic assumptions about the rules in \mathcal{A} and \mathcal{C} .

First we need to define a more general satisfaction relation than $S \models \varphi \Rightarrow^{\forall} \varphi'$. Let $\delta \in \{+, *\}$ be a flag and let $n \in \mathbb{N}$ be a natural number. We define a new satisfaction relation $S \models_n^{\delta} \varphi \Rightarrow^{\forall} \varphi'$ by restricting the paths in the definition of $S \models \varphi \Rightarrow^{\forall} \varphi'$ to length at most n , and requiring progress (at least one step) when $\delta = +$.

$$\begin{aligned}
step(c, c') &\equiv \bigvee_{\mu \equiv \varphi_l \Rightarrow \exists \varphi_r \in \mathcal{S}} \exists FreeVars(\mu) (\varphi_l[c/\square] \wedge \varphi_r[c'/\square]) \\
coreach(\varphi) &\equiv \forall n \forall c_0 \dots c_n \left(\square = c_0 \rightarrow \bigwedge_{0 \leq i < n} step(c_i, c_{i+1}) \rightarrow \neg \exists c_{n+1} step(c_n, c_{n+1}) \rightarrow \bigvee_{0 \leq i \leq n} \varphi[c_i/\square] \right)
\end{aligned}$$

Fig. 2. FOL encoding of one step transition relation and all-path reachability.

Formally, we define $\mathcal{S} \models_n^\delta \varphi \Rightarrow^\forall \varphi'$ to hold iff for any complete path $\tau = \gamma_1 \dots \gamma_k$ of length $k \leq n$ and for any ρ such that $(\gamma_1, \rho) \models \varphi$, there exists $i \in \{1, \dots, k\}$ such that $(\gamma_i, \rho) \models \varphi'$. Additionally, when $\delta = +$, we require that $i \neq 1$ (i.e. γ makes progress). The indexing on n is required to prove the soundness of circularities. Now we can state the soundness lemma.

Lemma 1. *If $\mathcal{S}, \mathcal{A} \vdash_C \varphi \Rightarrow^\forall \varphi'$ and $\mathcal{S} \models_n^+ \mathcal{A}$ and $\mathcal{S} \models_{n-1}^+ C$ then $\mathcal{S} \models_n^* \varphi \Rightarrow^\forall \varphi'$, and furthermore, if C is nonempty then $\mathcal{S} \models_n^+ \varphi \Rightarrow^\forall \varphi'$.*

Theorem 1 follows by showing that $\mathcal{S} \models \varphi \Rightarrow^\forall \varphi'$ iff $\mathcal{S} \models_n \varphi \Rightarrow^\forall \varphi'$ for all $n \in \mathbb{N}$.

Lemma 1 is proved by induction on the derivation (with each induction hypothesis universally quantified over n). CONSEQUENCE, CASE ANALYSIS, and ABSTRACTION are easy. AXIOM may only be used in cases where $\mathcal{S} \models_n^+ \mathcal{A}$ includes $\mathcal{S} \models_n^+ \varphi \Rightarrow^\forall \varphi'$ (as \mathcal{S} contains only one-path rules). REFLEXIVITY may only be used when C is empty, and $\mathcal{S} \models^* \varphi \Rightarrow^\forall \varphi'$ unconditionally. The premises of STEP are pattern implications which imply that any configuration matching φ is not stuck in $\Rightarrow_{\mathcal{S}}^T$, and all of its immediate successors satisfy φ' . This directly establishes that $\mathcal{S} \models^+ \varphi \Rightarrow^\forall \varphi'$. TRANSITIVITY requires considering execution paths more carefully. If C is empty, then the proof is trivial. Otherwise the induction hypothesis gives that $\varphi_1 \Rightarrow^\forall \varphi_2$ holds with progress. Therefore, when proving $\varphi_2 \Rightarrow^\forall \varphi_3$, the circularities are enabled soundly. CIRCULARITY proceeds by an inner well-founded induction on n . The outer induction over the derivation gives an induction hypothesis showing the desired conclusion under the additional assumption that $\varphi \Rightarrow^\forall \varphi'$ holds for any m strictly less than n , which is exactly the induction hypothesis provided by the inner induction on n . \square

We next show relative completeness: any valid all-path reachability property of any program in any language with an operational semantics given as a reachability system \mathcal{S} is derivable with the proof system in Fig. 1 from \mathcal{S} . As with Hoare and dynamic logics, “relative” means we assume an oracle capable of establishing validity in the first-order theory of the state, which here is the configuration model \mathcal{T} . An immediate consequence of relative completeness is that CIRCULARITY is sufficient to derive any repetitive behavior occurring in any program written in any language, and that STEP is also sufficient to derive any non-deterministic behavior! We establish the relative completeness under the following assumptions: (1) \mathcal{S} is finite; (2) the model \mathcal{T} includes natural numbers with addition and multiplication; and (3) the set of configurations \mathcal{T}_{Cfg} is countable (the model \mathcal{T} includes some injective function $\alpha : \mathcal{T}_{Cfg} \rightarrow \mathbb{N}$). Assumption (1) ensures STEP has a finite number of prerequisites. Assumption (2) is a standard assumption (also made by Hoare and dynamic logic completeness results) which allows the definition of Gödel’s β predicate. Assumption (3) allows the encoding of a sequence of configurations into a

IMP language syntax	IMP evaluation contexts syntax
$PVar ::= \text{program variables}$ $Exp ::= PVar \mid Int \mid Exp \text{ op } Exp$ $Stmt ::= \text{skip} \mid PVar := Exp$ $\quad \mid Stmt; Stmt \mid Stmt \parallel Stmt$ $\quad \mid \text{if}(Exp) Stmt \text{ else } Stmt$ $\quad \mid \text{while}(Exp) Stmt$	$Context ::= \blacksquare$ $\quad \mid \langle Context, State \rangle$ $\quad \mid Context \text{ op } Exp \mid Int \text{ op } Context$ $\quad \mid PVar := Context \mid Context; Stmt$ $\quad \mid Context \parallel Stmt \mid Stmt \parallel Context$ $\quad \mid \text{if}(Context) Stmt \text{ else } Stmt$
IMP operational semantics	
lookup $\langle C, \sigma \rangle[x] \Rightarrow^3 \langle C, \sigma \rangle[\sigma(x)]$ asgn $\langle C, \sigma \rangle[x := i] \Rightarrow^3 \langle C, \sigma[x \leftarrow i] \rangle[\text{skip}]$ cond $\text{if}(i) s_1 \text{ else } s_2 \Rightarrow^3 s_1 \quad \text{if } i \neq 0$ while $\text{while}(e) s \Rightarrow^3 \text{if}(e) s; \text{while}(e) s \text{ else skip}$	op $i_1 \text{ op } i_2 \Rightarrow^3 i_1 \text{ op}_{Int} i_2$ seq $\text{skip}; s \Rightarrow^3 s$ cond ₂ $\text{if}(0) s_1 \text{ else } s_2 \Rightarrow^3 s_2$ finish $\text{skip} \parallel \text{skip} \Rightarrow^3 \text{skip}$

Fig. 3. IMP language syntax and operational semantics based on evaluation contexts.

sequence of natural numbers. We expect the operational semantics of any reasonable language to satisfy these conditions. Formally, we have the following

Theorem 2 (Relative Completeness). *If $S \models \varphi \Rightarrow^v \varphi'$ then $S \vdash \varphi \Rightarrow^v \varphi'$, for any semantics S satisfying the three assumptions above.*

Proof (sketch — complete details in [14]). Our proof relies on the fact that pattern reasoning in first-order matching logic reduces to FOL reasoning in the model \mathcal{T} . A key component of the proof is defining the $coreach(\varphi)$ predicate in plain FOL. This predicate holds when every complete $\Rightarrow^{\mathcal{T}_S}$ -path τ starting at c includes some configuration satisfying φ . We express $coreach(\varphi)$ using auxiliary predicate $step(c, c')$ which encodes the one step transition relation ($\Rightarrow^{\mathcal{T}_S}$). Fig. 2 shows both definitions. As it is, $coreach(\varphi)$ is not a proper FOL formula, as it quantifies over a sequence of configurations. This is addressed using the injective function α to encode universal quantification over a sequence of configurations into universal quantification over a sequence of integers, which is in turn encoded into quantification over two integer variables using Gödel's β predicate (encoding shown in [14]).

Next, using the definition above we encode the semantic validity of an all-path reachability rule as FOL validity: $S \models \varphi \Rightarrow^v \varphi'$ iff $\models \varphi \rightarrow coreach(\varphi')$. Therefore, the theorem follows by CONSEQUENCE from the sequent $S \vdash coreach(\varphi') \Rightarrow^v \varphi'$. We derive this sequent by using CIRCULARITY to add the rule to the set of circularities, then by using STEP to derive one $\Rightarrow^{\mathcal{T}_S}$ -step, and then by using TRANSITIVITY and AXIOM with the rule itself to derive the remaining $\Rightarrow^{\mathcal{T}_S}$ -steps (circularities can be used after TRANSITIVITY). The formal derivation uses all eight proof rules. \square

6 Verifying Programs

In this section we show a few examples of using our proof system to verify programs based on an operational semantics. In a nutshell, the proof system enables generic symbolic execution combined with circular reasoning. Symbolic execution is achieved by rewriting modulo domain reasoning.

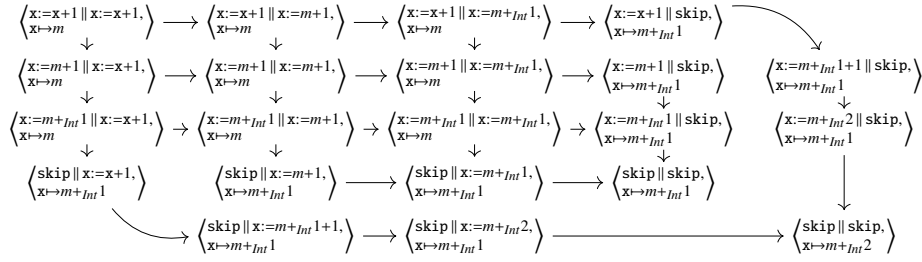


Fig. 4. State space of the racing increment example

First, we introduce a simple parallel imperative language, IMP. Fig. 3 shows its syntax and an operational semantics based on evaluation contexts [11] (we choose evaluation contexts for presentation purposes only). IMP has only integer expressions. When used as conditions of `if` and `while`, zero means false and any non-zero integer means true (like in C). Expressions are formed with integer constants, program variables, and conventional arithmetic constructs. Arithmetic operations are generically described as `op`. IMP statements are assignment, `if`, `while`, sequential composition and parallel composition. IMP has shared memory parallelism without explicit synchronization. The examples use the parallel construct only at the top-level of the programs. The second example shows how to achieve synchronization using the existing language constructs.

The program configurations of IMP are pairs $\langle \text{code}, \sigma \rangle$, where `code` is a program fragment and σ is a state term mapping program variables into integers. As usual, we assume appropriate definitions for the integer and map domains available, together with associated operations like arithmetic operations ($i_1 \text{ op}_{Int} i_2$, etc.) on the integers and lookup ($\sigma(x)$) and update ($\sigma[x \leftarrow i]$) on the maps. We also assume a context domain with a plugging operation ($C[t]$) that composes a context and term back into a configuration. A configuration context consists of a code context and a state. The definition in Fig. 3 consists of eight reduction rules between program configurations, which make use of first-order variables: x is a variable of sort *PVar*; e is a variable of sort *Exp*; s, s_1, s_2 are variables of sort *Stm*; i, i_1, i_2 are variables of sort *Int*; σ is a variable of sort *State*; C is a variable of sort *Context*. A rule reduces a configuration by splitting it into a context and a redex, rewriting the redex and possibly the context, and then plugging the resulting term into the resulting context. As an abbreviation, a context is not mentioned if not used; e.g., the rule `op` is in full $\langle C, \sigma \rangle [i_1 \text{ op } i_2] \Rightarrow^{\exists} \langle C, \sigma \rangle [i_1 \text{ op}_{Int} i_2]$. For example, configuration $\langle x := (2 + 5) - 4, \sigma \rangle$ reduces to $\langle x := 7 - 4, \sigma \rangle$ by applying the `op+` rule with $C \equiv x := \blacksquare - 4$, $\sigma \equiv \sigma$, $i_1 \equiv 2$ and $i_2 \equiv 5$. We can regard the operational semantics of IMP above as a set of reduction rules of the form “ $l \Rightarrow^{\exists} r$ if b ”, where l and r are program configurations with variables constrained by boolean condition b . As discussed in Section 3, our proof system works with any rules of this form.

Next, we illustrate the proof system on a few examples. The first example shows that our proof system enables exhaustive state exploration, similar to symbolic model-checking but based on the operational semantics. Although humans prefer to avoid such explicit proofs and instead methodologically use abstraction or compositional reasoning whenever possible (and such methodologies are not excluded by our proof system), a

complete proof system must nevertheless support them. The code $x := x + 1 \parallel x := x + 1$ exhibits a race on x : the value of x increases by 1 when both reads happen before either write, and by 2 otherwise. The all-path rule that captures this behavior is

$$\langle x := x + 1 \parallel x := x + 1, x \mapsto m \rangle \Rightarrow^{\forall} \exists n (\langle \text{skip}, x \mapsto n \rangle \wedge (n = m +_{Int} 1 \vee n = m +_{Int} 2))$$

We show that the program has exactly these behaviors by deriving this rule in the proof system. Call the right-hand-side pattern G . The proof contains subproofs of $c \Rightarrow^{\forall} G$ for every reachable configuration c , tabulated in Fig. 4. The subproofs for c matching G use REFLEXIVITY and CONSEQUENCE, while the rest use TRANSITIVITY, STEP, and CASE ANALYSIS to reduce to the proofs for the next configurations. For example, the proof fragment below shows how $\langle x := m + 1 \parallel x := x + 1, x \mapsto m \rangle \Rightarrow^{\forall} G$ reduces to $\langle x := m +_{Int} 1 \parallel x := x + 1, x \mapsto m \rangle \Rightarrow^{\forall} G$ and $\langle x := m + 1 \parallel x := m + 1, x \mapsto m \rangle \Rightarrow^{\forall} G$:

$$\frac{\text{STEP} \frac{\dots}{\langle x := m + 1 \parallel x := x + 1, x \mapsto m \rangle \Rightarrow^{\forall} \langle x := m +_{Int} 1 \parallel x := x + 1, x \mapsto m \rangle} \vee \frac{\dots}{\langle x := m + 1 \parallel x := m + 1, x \mapsto m \rangle \Rightarrow^{\forall} \langle x := m + 1 \parallel x := m + 1, x \mapsto m \rangle}}{\langle x := m + 1 \parallel x := x + 1, x \mapsto m \rangle \Rightarrow^{\forall} G} \text{CA} \quad \frac{\dots}{\langle x := m +_{Int} 1 \parallel x := x + 1, x \mapsto m \rangle \Rightarrow^{\forall} G} \vee \frac{\dots}{\langle x := m + 1 \parallel x := m + 1, x \mapsto m \rangle \Rightarrow^{\forall} G} \text{TRANS}}{\langle x := m + 1 \parallel x := x + 1, x \mapsto m \rangle \Rightarrow^{\forall} G}$$

For the rule hypotheses of STEP above, note that all rules but **lookup** and **op₊** make the overlap condition $\exists c \left(\left\langle \frac{x := m + 1 \parallel x := x + 1}{x \mapsto m} \right\rangle [c/\square] \wedge \varphi_l [c/\square] \right)$ unsatisfiable, and only one choice of free variables works for the **lookup** and **op₊** rules. For **lookup**, φ_l is $\langle C, \sigma \rangle [x]$ and the overlap condition is only satisfiable if the logical variables C , σ and x are equal to $\langle x := m + 1 \parallel x := \blacksquare + 1 \rangle$, $\langle x \mapsto m \rangle$, and x , resp. Under this assignment, the pattern $\varphi_r = \langle C, \sigma \rangle [\sigma(x)]$ is equivalent to $\langle x := m + 1 \parallel x := m + 1, x \mapsto m \rangle$, the right branch of the disjunction. The **op₊** rule is handled similarly. The assignment for **lookup** can also witness the existential in the progress hypothesis of STEP. Subproofs for other states in Fig. 4 can be constructed similarly.

The next two examples use loops and thus need to state and prove invariants. As discussed in [4], CIRCULARITY generalizes the various language-specific invariant proof rules encountered in Hoare logics. One application is reducing a proof of

$\varphi \Rightarrow^{\forall} \varphi'$ to proving $\varphi_{inv} \Rightarrow^{\forall} \varphi_{inv} \vee \varphi'$ for some pattern invariant φ_{inv} . We first show $\models \varphi \rightarrow \varphi_{inv}$, and use CONSEQUENCE to change the goal to $\varphi_{inv} \Rightarrow^{\forall} \varphi'$. This is claimed as a circularity, and then proved by transitivity with $\varphi_{inv} \vee \varphi'$. The second hypothesis $\{\varphi_{inv} \Rightarrow^{\forall} \varphi'\} \vdash \varphi_{inv} \vee \varphi' \Rightarrow^{\forall} \varphi'$ is proved by CASE ANALYSIS, AXIOM, and REFLEXIVITY.

Next, we can use Peterson's algorithm for mutual exclusion to eliminate the race as shown in Fig. 5. The all-path rule $\varphi \Rightarrow^{\forall} \varphi'$ that captures the new behavior is

$$\begin{aligned} & \langle T_0 \parallel T_1, (f_0 \mapsto 0, f_1 \mapsto 0, x \mapsto N) \rangle \\ & \Rightarrow^{\forall} \exists t \langle \text{skip}, (f_0 \mapsto 0, f_1 \mapsto 0, x \mapsto N +_{Int} 2, \text{turn} \mapsto t) \rangle \end{aligned}$$

Similarly to the unsynchronized example, the proof contains subproofs of $c \Rightarrow^{\forall} \varphi'$ for every configuration c reachable from φ . The main difference is that CIRCULARITY is used with each of these rules $c \Rightarrow^{\forall} \varphi'$ with one of the two threads of c in the while loop

<pre>f0 = 1; turn = 1; while (f1 && turn) skip x = x + 1; f0 = 0;</pre>	<pre>f1 = 1; turn = 0; while (f0 && (1 - turn)) skip x = x + 1; f1 = 0;</pre>
---	---

Fig. 5. Peterson's algorithm (threads T_0 and T_1)

(these rules capture the invariants). Thus, when we reach a configuration c visited before, we use the rule added by CIRCULARITY to complete the proof.

The final example is the program $\text{SUM} \equiv \text{“s} := 0; \text{ LOOP”}$ (where LOOP stands for “while ($n > 0$) ($s := s + n; n := n - 1$)”), which computes in s the sum of the numbers from 1 up to n . The all-path reachability rule $\varphi \Rightarrow^V \varphi'$ capturing this behavior is

$$\langle \text{SUM}, (s \mapsto s, n \mapsto n) \rangle \wedge n \geq_{\text{Int}} 0 \Rightarrow^V \langle \text{skip}, (s \mapsto n *_{\text{Int}} (n +_{\text{Int}} 1) /_{\text{Int}} 2, n \mapsto 0) \rangle$$

We derive the above rule in our proof system by using CIRCULARITY with the invariant rule $\exists n' (\langle \text{LOOP}, (s \mapsto (n -_{\text{Int}} n') *_{\text{Int}} (n +_{\text{Int}} n' +_{\text{Int}} 1) /_{\text{Int}} 2, n \mapsto n') \rangle \wedge n' \geq_{\text{Int}} 0) \Rightarrow^V \varphi'$. Previous work [4–7] presented a proof system able to derive similar rules, but which hold along *some* execution path, requiring a separate proof that the program is deterministic.

7 Implementation

Here we briefly discuss our prototype implementation of the proof system in Fig. 1 in \mathbb{K} [9]. We choose \mathbb{K} because it is a modular semantic language design framework, it is used for teaching programming languages at several universities, and there are several languages defined in it including C [10], PHP [15], Python, and Java. Due to space limitations, we do not present \mathbb{K} here. We refer the reader to <http://kframework.org> for language definitions, a tutorial, and our prototype. As discussed in Section 3, we simply view a \mathbb{K} semantics as a set of reachability rules of the form “ $l \wedge b \Rightarrow^{\exists} r$ ”.

The prototype is implemented in Java, and uses Z3 [16] for domain reasoning. It takes an operational semantics and uses it to perform concrete or symbolic execution. At its core, it performs *narrowing* of a conjunctive pattern with reachability rules between conjunctive patterns, where a conjunctive pattern is a pattern of the form $\exists X(\pi \wedge \psi)$, with X a set of variables, π a basic pattern (program configurations with variables), and ψ a structureless formula. Narrowing is necessary when a conjunctive pattern is too abstract to match the left-hand side of any rule, but is unifiable with the left-hand sides of some rules. For instance, consider the IMP code fragment “if (b) then $x = 1$; else $x = 0$;”. This code does not match the left-hand sides of either of the two rules giving semantics to if (similar to **cond**₁ and **cond**₂ in Fig. 3), but it is unifiable with the left-hand sides of both rules. Intuitively, if we use the rules of the semantics, taking steps of rewriting on a ground configuration yields concrete execution, while taking steps of narrowing on a conjunctive pattern yields symbolic execution. In our practical evaluation, we found that conjunctive patterns tend to suffice to specify both the rules for operational semantics and program specifications.

For each step of narrowing, the \mathbb{K} engine uses *unification modulo theories*. In our implementation, we distinguish a number of mathematical theories (e.g. booleans, integers, sequences, sets, maps, etc) which the underlying SMT solver can reason about. Specifically, when unifying a conjunctive pattern $\exists X(\pi \wedge \psi)$ with the left-hand side of a rule $\exists X_l(\pi_l \wedge \psi_l)$ (we assume $X \cap X_l = \emptyset$), the \mathbb{K} engine begins with the syntactic unification of the basic patterns π and π_l . Upon encountering corresponding subterms (π' in π and π'_l in π_l) which are both terms of one of the theories above, it records an equality $\pi' = \pi'_l$ rather than decomposing the subterms further (if one is in a theory, and the other one is in a different theory or is not in any theory, the unification fails). If this stage of

unification is successful, we end up with a conjunction ψ_u of constraints, some having a variable in one side and some with both sides in one of the theories. Satisfiability of $\exists X \cup X_l(\psi \wedge \psi_u \wedge \psi_l)$ is then checked by the SMT solver. If it is satisfiable, then narrowing takes a step from $\exists X(\pi \wedge \psi)$ to $\exists X \cup X_l \cup X_r(\pi_r \wedge \psi \wedge \psi_u \wedge \psi_l \wedge \psi_r)$, where $\exists X_r(\pi_r \wedge \psi_r)$ is the right-hand side of the rule. Intuitively, “collecting” the constraints $\psi_u \wedge \psi_l \wedge \psi_r$ is similar to collecting the path constraint in traditional symbolic execution (but is done in a language-generic manner). For instance, in the `if` case above, narrowing with the two semantics rules results in collecting the constraints $b = \text{true}$ and $b = \text{false}$.

The \mathbb{K} engine accepts a set of user provided rules to prove together, which capture the behavior of the code being verified. Typically, these rules specify the behavior of recursive functions and while loops. For each rule, the \mathbb{K} engine searches starting from the left-hand side for formulae which imply the right-hand side, starting with \mathcal{S} the semantics and \mathcal{C} all the rules it attempts to prove. By a derived rule called *Set Circularity*, this suffices to show that each rule is valid. As an optimization, AXIOM is given priority over STEP (use specifications rather than stepping into the code).

Most work goes into implementing the STEP proof rule, and in particular calculating how $\rho \models \exists c(\varphi[c/\square] \wedge \varphi_l[c/\square])$ can be satisfied. This holds when $\rho^\gamma \models \varphi$ and $\rho^\gamma \models \varphi_l$, which can be checked with unification modulo theories. To use STEP in an automated way, the \mathbb{K} tool constructs φ' for a given φ as a disjunction of $\varphi_r \wedge \psi_u \wedge \psi \wedge \psi_l$ over each rule $\varphi_l \Rightarrow^{\exists} \varphi_r \in \mathcal{S}$ and each way ψ_u of unifying φ with φ_l . As discussed in Section 4, in general this disjunction may not be finite, but it is sufficient for the examples that we considered. The CONSEQUENCE proof rule also requires unification modulo theories, to check validity of the implication hypothesis $\models \varphi_1 \rightarrow \varphi'_1$. The main difference from STEP is that the free variables of φ' become universality quantified when sending the query to the SMT solver. The implementation of the other proof rules is straight-forward.

8 Related Work

Using Hoare logic [17] to prove concurrent programs correct dates back to Owicki and Gries [18]. In the rely-guarantee method proposed by Jones [19] each thread relies on some properties being satisfied by the other threads, and in its turn, offers some guarantees on which the other threads can rely. O’Hearn [20] advances a Separation Hypothesis in the context of separation logic [21] to achieve compositionality: the state can be partitioned into separate portions for each process and relevant resources, respectively, satisfying certain invariants. More recent research focuses on improvements over both of the above methods and even combinations of them (e.g., [22–25]).

The satisfaction of all-path-reachability rules can also be understood intuitively in the context of temporal logics. Matching logic formulae can be thought of as state formulae, and reachability rules as temporal formulae. Assuming CTL* on finite traces, the semantics rule $\varphi \Rightarrow^{\exists} \varphi'$ can be expressed as $\varphi \rightarrow E\bigcirc\varphi'$, while an all-path reachability rule $\varphi \Rightarrow^{\forall} \varphi'$ can be expressed as $\varphi \rightarrow A\bigtriangleleft\varphi'$. However, unlike in CTL*, the φ and φ' formulae of reachability rules $\varphi \Rightarrow^{\exists} \varphi'$ or $\varphi \Rightarrow^{\forall} \varphi'$ share their free variables. Thus, existing proof systems for temporal logics (e.g., the CTL* one by Pnueli and Kesten) are not directly comparable with our approach.

Bae et al [26], Rocha and Meseguer [27], and Rocha et al [28] use narrowing to perform symbolic reachability analysis in a transition system associated to a unconditional rewrite theory for the purposes of verification. There are two main differences

between their work and ours. First, they express state predicates in equational theories. Matching logic is more general, being first-order logic over a model of configurations T . Consequently, the `STEP` proof rule takes these issues into account when considering the successors of a state. Second, they use rewrite systems for symbolic model checking. Our work is complementary, in the sense that we use the operational semantics for program verification, and check properties more similar to those in Hoare logic.

Language-independent proof systems A first proof system is introduced in [6], while [5] presents a mechanical translation from Hoare logic proof derivations for IMP into derivations in the proof system. The `CIRCULARITY` proof rule is introduced in [4]. Finally, [7] supports operational semantics given with conditional rules, like small-step and big-step. All these previous results can only be applied to deterministic programs.

9 Conclusion and Future Work

This paper introduces a sound and (relatively) complete language-independent proof system which derives program properties holding along all execution paths (capturing partial correctness for non-deterministic programs), directly from an operational semantics. The proof system separates reasoning about deterministic language features (via the operational semantics) from reasoning about non-determinism (via the proof system). Thus, all we need in order to verify programs in a language is an operational semantics for the respective language.

We believe that existing techniques such as rely-guarantee and concurrent separation logic could be used in conjunction with our proof system to achieve semantically grounded and compositional verification.

Our approach handles operational semantics given with unconditional rules, like \mathbb{K} framework, PLT-Redex, and CHAM, but it cannot handle operational semantics given with conditional rules, like big-step and small-step (rules with premises). Extending the presented results to work with conditional rules would boil down to extending the `STEP` proof rule, which derives the fact that φ reaches φ' in one step along all execution paths. Such an extended `STEP` would have as prerequisites whether the left-hand side of a semantics rule matches (like the existing `STEP`) and additionally whether its premises hold. The second part would require an encoding of reachability in first-order logic, which is non-trivial and mostly likely would result in a first-order logic over a richer model than \mathcal{T} . The difficulty arises from the fact that `STEP` must ensure all successors of φ are in φ' . Thus, this extension is left as future work.

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References

1. Nipkow, T.: Winskel is (almost) right: Towards a mechanized semantics textbook. *Formal Aspects of Computing* **10** (1998) 171–186

2. Jacobs, B.: Weakest pre-condition reasoning for Java programs with JML annotations. *J. Logic and Algebraic Programming* **58**(1-2) (2004) 61–88
3. Appel, A.W.: Verified software toolchain. In: ESOP. Volume 6602 of LNCS. (2011) 1–17
4. Roşu, G., Ştefănescu, A.: Checking reachability using matching logic. In: OOPSLA, ACM (2012) 555–574
5. Roşu, G., Ştefănescu, A.: From Hoare logic to matching logic reachability. In: FM. Volume 7436 of LNCS. (2012) 387–402
6. Roşu, G., Ştefănescu, A.: Towards a unified theory of operational and axiomatic semantics. In: ICALP. Volume 7392 of LNCS. (2012) 351–363
7. Roşu, G., Ştefănescu, A., Ciobăcă, c., Moore, B.M.: One-path reachability logic. In: LICS'13, IEEE (2013)
8. Roşu, G., Ellison, C., Schulte, W.: Matching logic: An alternative to Hoare/Floyd logic. In: AMAST. Volume 6486 of LNCS. (2010) 142–162
9. Roşu, G., Şerbănuţă, T.F.: An overview of the K semantic framework. *J. Logic and Algebraic Programming* **79**(6) (2010) 397–434
10. Ellison, C., Roşu, G.: An executable formal semantics of C with applications. In: POPL, ACM (2012) 533–544
11. Felleisen, M., Findler, R.B., Flatt, M.: *Semantics Engineering with PLT Redex*. MIT (2009)
12. Berry, G., Boudol, G.: The chemical abstract machine. *Theoretical Computer Science* **96**(1) (1992) 217–248
13. Matthews, J., Findler, R.B.: An operational semantics for Scheme. *JFP* **18**(1) (2008) 47–86
14. Ştefănescu, A., Ciobăcă, c., Moore, B.M., Şerbănuţă, T.F., Roşu, G.: Reachability Logic in K. Technical Report <http://hdl.handle.net/2142/46296>, University of Illinois (Nov 2013)
15. Filaretto, D., Maffei, S.: An executable formal semantics of php. In: ECOOP. LNCS (2014) to appear.
16. de Moura, L.M., Bjørner, N.: Z3: An efficient SMT solver. In: TACAS. Volume 4963 of LNCS. (2008)
17. Hoare, C.A.R.: An axiomatic basis for computer programming. *Communications of the ACM* **12**(10) (1969) 576–580
18. Owicki, S.S., Gries, D.: Verifying properties of parallel programs: An axiomatic approach. *Communications of the ACM* **19**(5) (1976) 279–285
19. Jones, C.B.: Specification and design of (parallel) programs. In Mason, R.E.A., ed.: *Information Processing 1983: World Congress Proceedings*, Elsevier (1984) 321–332
20. O'Hearn, P.W.: Resources, concurrency, and local reasoning. *Theoretical Computer Science* **375**(1-3) (2007) 271–307
21. Reynolds, J.C.: Separation logic: A logic for shared mutable data structures. In: LICS, IEEE (2002) 55–74
22. Feng, X.: Local rely-guarantee reasoning. In: POPL, ACM (2009) 315–327
23. Vafeiadis, V., Parkinson, M.J.: A marriage of rely/guarantee and separation logic. In: CONCUR. Volume 4703 of LNCS. (2007) 256–271
24. Reddy, U.S., Reynolds, J.C.: Syntactic control of interference for separation logic. In: POPL, ACM (2012) 323–336
25. Hayman, J.: Granularity and concurrent separation logic. In: CONCUR. Volume 6901 of LNCS. (2011) 219–234
26. Bae, K., Escobar, S., Meseguer, J.: Abstract logical model checking of infinite-state systems using narrowing. In: RTA. (2013) 81–96
27. Rocha, C., Meseguer, J.: Proving safety properties of rewrite theories. In: CALCO. Volume 6859 of LNCS. (2011) 314–328
28. Rocha, C., Meseguer, J., Muñoz, C.A.: Rewriting modulo smt and open system analysis. In: WRLA. LNCS (2014) to appear.